

REMARKS

Neither of the cited references teach a multiplexer to multiplex said lower data rate data and control information and transmit the data and control information externally of said integrated circuit.

With respect to the reference to Yukutake, it appears that the Examiner is taking the position that somehow the use of the isolators 501 and 502, in Figure 1, teach the use of separate circuits. But, even if one were to accept this relatively unusual proposition, it still fails to meet the claimed invention.

This is because there is a fundamental difference in the way the circuits in Yukutake and the claimed invention work. In the cited reference, the so-called “decimeter” 515 is apparently asserted to reduce the higher data rate. But, even if that is so, it also serializes the data. See column 7, lines 49-50. Thus, it is assumed that the device 515 is read to be the device for reducing the higher data rate in claim 1, as well as the multiplexer to multiplex said lower data rate data and control information. However, the claim further calls for a second integrated circuit which, apparently, the Examiner contends to be the stuff downstream of the isolator 502 by virtual of isolator 502 allegedly functioning as if there were a separate integrated circuit.

The problem is that the claim requires that the second integrated circuit include a demultiplexer to demultiplex the lower data rate. In the cited reference, all of the data going through the ADCR 516 in-DSP 517, RXDR 518, ex-DSP 536 and returning through the TXDR 521, in-DSP 517 is still serial data. The serial conversion is done in the DACR 522. See column 7, line 65. In other words, there does not appear to be any type of demultiplexing or de-serialization on the other side of the isolators 502 and 501 from the DCM 515 and INT 523. Thus, there is no isolation between serialized and unserialized data. All the serialized data is handled on the same side of the isolators.

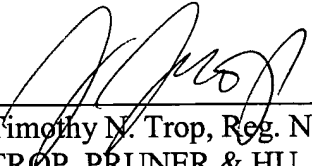
In order for the analogy the Examiner is asserting to work, he would have to show that on the other side of the isolator 501, the data is de-serialized, for example, on ADCR 516. However, there is no indication that this is done in the cited reference.

Kanekawa cannot help with this deficiency since Kanekawa does not teach reducing the data rate. See the office action at page 7, lines 9-12. That is why the Examiner is relying on Yukutake.

Therefore, reconsideration of the rejection of the claims is respectfully requested.

Respectfully submitted,

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